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| | | | 2. | Context threading: a flexible and efficient dispatch technique for virtual machine interpring Berndl, M.; Vitale, B.; Zaleski, M.; Brown, A.D.; Code Generation and Optimization. 2005. CGO 2005. International Symposium on 20-23 March 2005 Page(s):15 - 26 Digital Object Identifier 10.1109/CGO.2005.14 | reter: | |
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| | | | 3. | S. Fair cache sharing and partitioning in a chip multiprocessor architecture Kim, S.; Chandra, D.; Solihin, Y.; Parallel Architecture and Compilation Techniques, 2004, PACT 2004, Proceedings, 13th Interr 29 Sept3 Oct. 2004 Page(s):111 - 122 Digital Object Identifier 10.1109/PACT.2004.1342546 | natioi | |
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| | · | | 4. | 4. Hardware support for control transfers in code caches Kim, HS.; Smith, J.E.; Microarchitecture. 2003. MICRO-36, Proceedings. 36th Annual IEEE/ACM International Sympology Page(s):253 - 264 Digital Object Identifier 10.1109/MICRO.2003.1253200 AbstractPlue I Full Taxt: PDE/MANKR. IEEE CNE | <u>osiur</u> | |
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| | | Ē | 5. | 5. A dynamic multithreading processor Akkary, H.; Driscoll, M.A.; Microarchitecture, 1998, MICRO-31, Proceedings, 31st Annual ACM/IEEE International Symposistic Object Identifier 10.1109/MICRO.1998.742784 | osiur | |

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6. A dualthreaded Java processor for Java multithreading

Chun-Mok Chung; Shin-Dug Kim;

<u>Parallel and Distributed Systems. 1998, Proceedings., 1998 International Conference on</u>
14-16 Dec. 1998 Page(s):693 - 700

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